

J-382 U.S. PTO
10/034163
12/27/01

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U.S. UTILITY Patent Application

PATENT NUMBER and
ISSUE DATE

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|---|---------------------------|---|---------------|----------------------------------|----------------------|
| APPL NUM 10034163 | FILING DATE 12/27/2001 | CLASS 438 | SUBCLASS 3 | GAU 2812 | EXAMINER S. FOONG |
| **APPLICANTS: Jeon Joong; 2823 | | | | | |
| **CONTINUING DATA VERIFIED: | | | | | |
| ** FOREIGN APPLICATIONS VERIFIED: | | | | | |
| PG-PUB <input type="checkbox"/> | | DO NOT PUBLISH <input type="checkbox"/> | | RESCIND <input type="checkbox"/> | |
| Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no Verified and Acknowledged Examiners's initials | | | | ATTORNEY DOCKET NO G0518 | |
| TITLE : Preparation of stack high-K gate dielectrics with nitrided layer | | | | | |

U.S. DEPT. OF COMM./PAT. & TM.-PTO-436L (Rev. 12-94)

| | | | |
|---|-----------|---------------------------|---|
| NOTICE OF ALLOWANCE MAILED | | CLAIMS ALLOWED | |
| | | Assistant Examiner | Total Claims: Print Claim for O.G. |
| ISSUE FEE | | Primary Examiner | DRAWING |
| Amount Due | Date Paid | | Sheets Drwg. Figs. Drwg. Print Fig. |
| <input type="checkbox"/> TERMINAL DISCLAIMER | | PREPARED FOR ISSUE | Application Examiner |
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